

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): S. MINAMI et al

Serial No.: 09/768,471

Filed: January 25, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND METHOD OF MANUFACTURING THE SAME

Group: 2815

Examiner: B. BAUMEISTER

TECHNOLOGY CENTER 2800

#1171D
Suppl
Amend
J. M. (Jan 21/03)
1/28/03
RECEIVED
JAN 21 2003

SUPPLEMENTING AMENDMENTAssistant Commissioner of Patents
Washington, D. C. 20231

Jan. 21, 2003

Sir:

Further to the responsive Amendment filed on December 23, 2002, the following additional amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS:Please **amend** claims 1, 29, 32, 35 and 36, as follows:

D1

1. (Four Times Amended) A semiconductor integrated circuit device comprising:
a semiconductor substrate of a second conductivity type;